



Patent  
Attorney's Docket No. 1009683-000501

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of

Tadayoshi Nakano et al.

Application No.: 10/807,450

Filed: March 24, 2004

For: SEMICONDUCTOR INTEGRATED  
CIRCUIT IN WHICH VOLTAGE  
DOWN CONVERTER OUTPUT

) **MAIL STOP AF**

) Group Art Unit: 2819

) Examiner: Linh V. Nguyen

) Confirmation No.: 3365

**AMENDMENT UNDER 37 C.F.R. §1.116**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated May 23, 2006, please amend the  
above-identified patent application as follows:

